

FIG. 1
 (Prior Art)

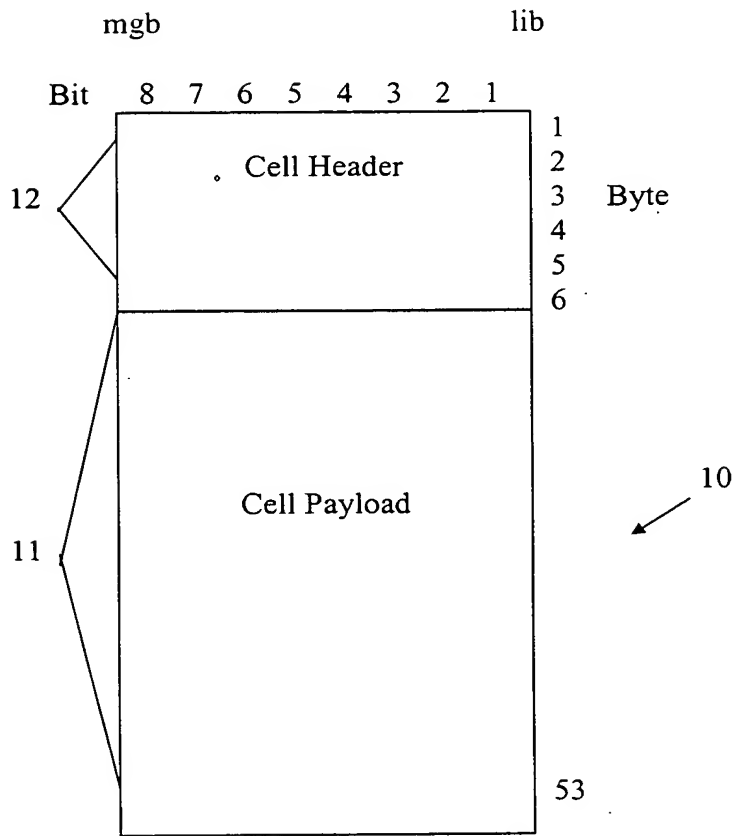


Fig. 1a

Fig. 1b

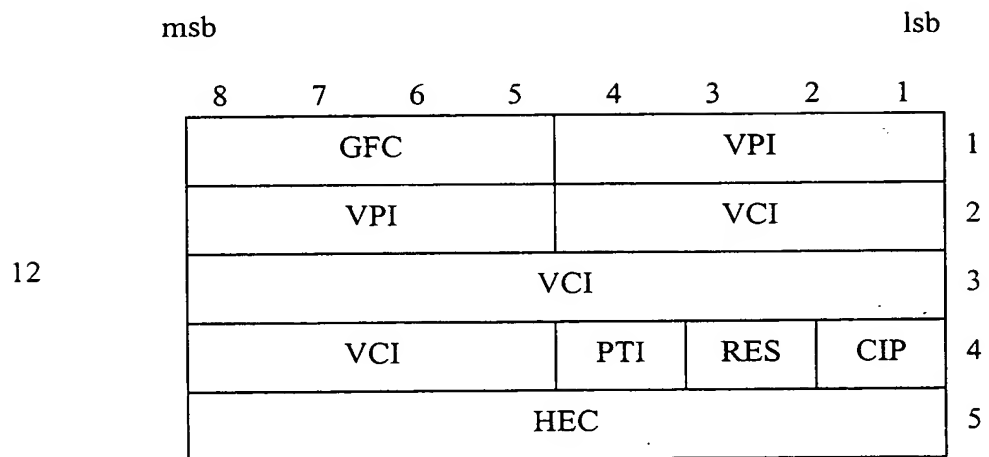


Diagram illustrating the frame structure and bit allocation:

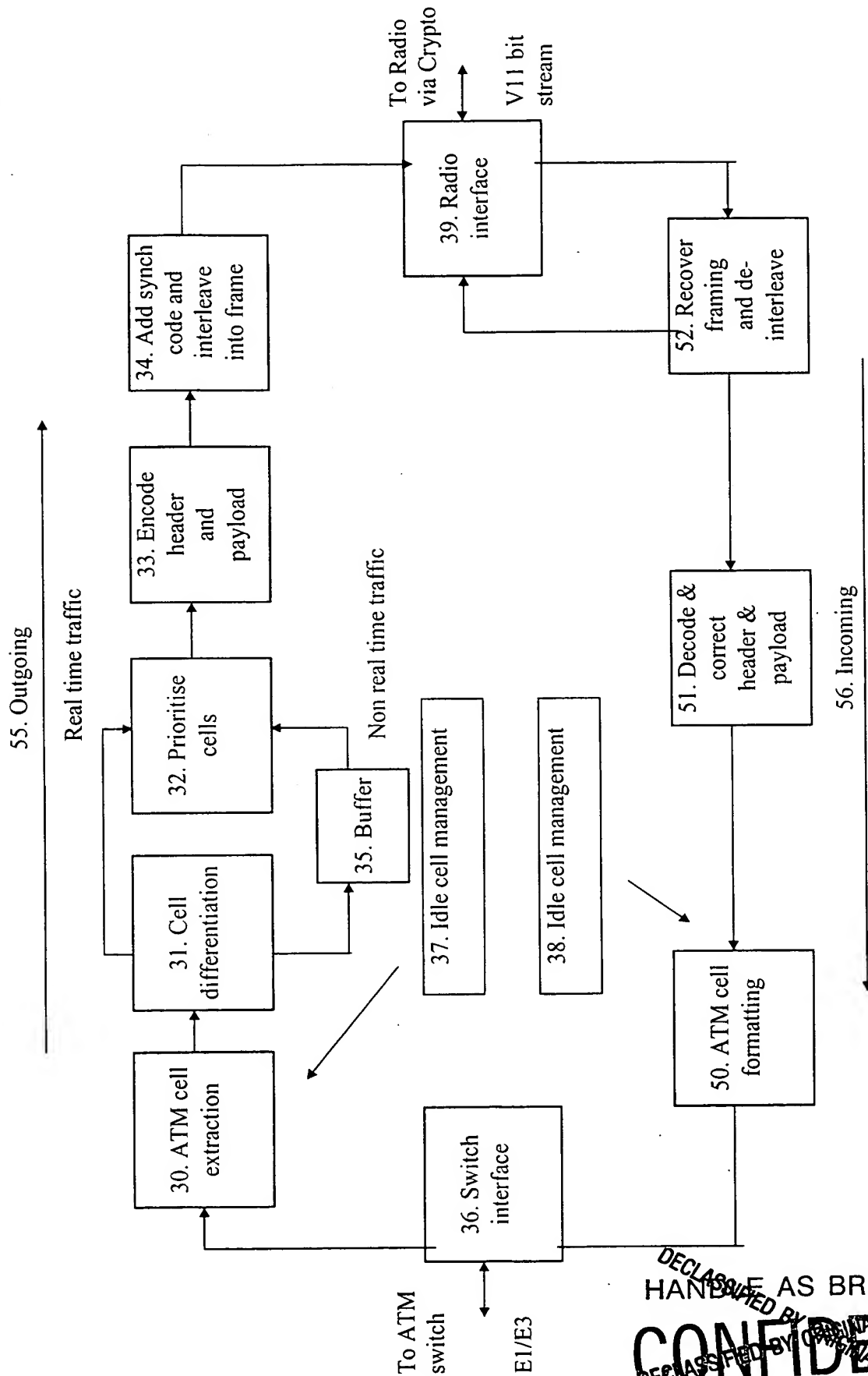
- Encoded payload:** $62 \times 8 = 496$ bits (labeled 20).
- Encoded header:** $(15 \times 4) + 4 = 64$ bits (labeled 21).
- Sync word:** 31 bits (labeled 22).

The frame is composed of these three parts, with "etc" indicating repetition. The total frame size is 591 bits.

The frame is divided into segments, with a detailed view of the frame end showing the following bit positions:

- 558th (black runs out)
- 570th (grey runs out)
- 591st (all bits used)

Fig. 3



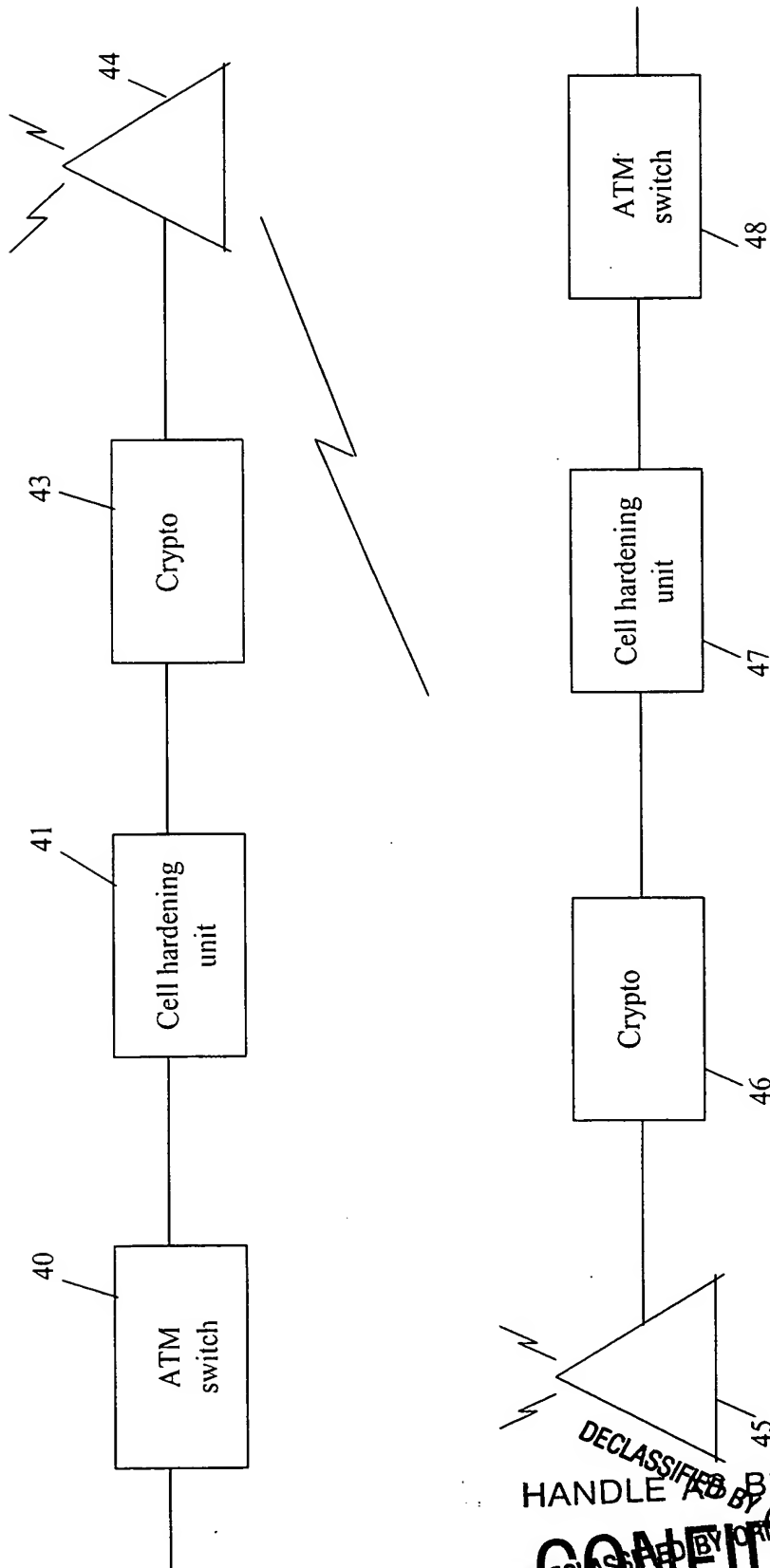


Fig. 4

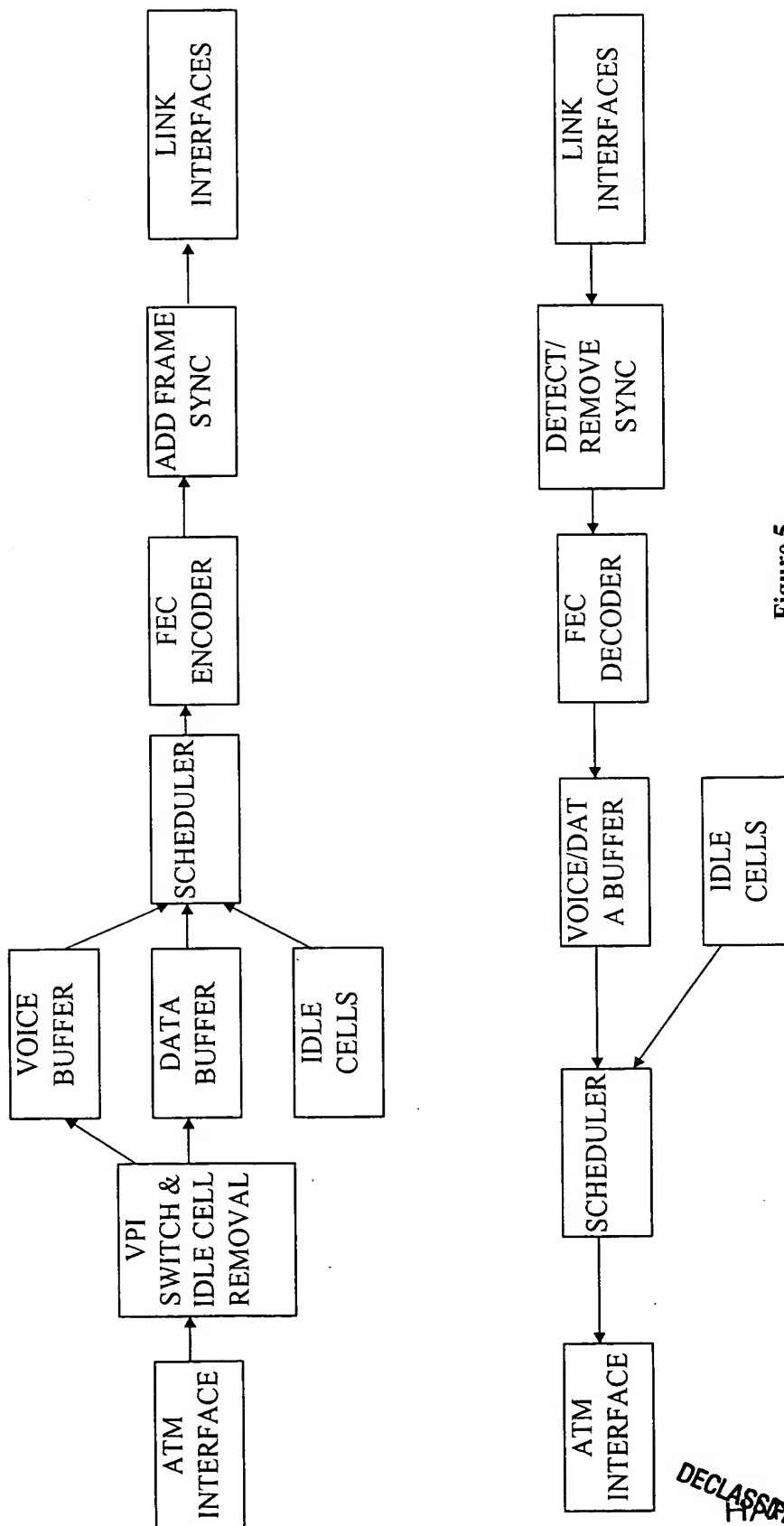


Figure 5.

